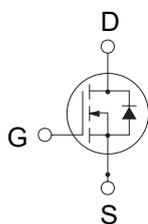
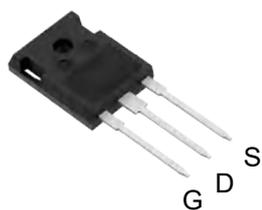
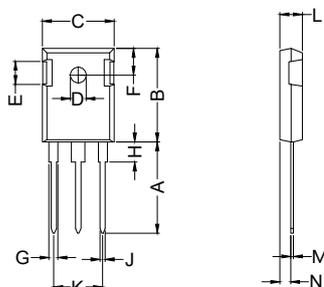


# SMOS50N25B2

## Power MOSFETs



Dimensions TO-247AD



Dim.	Millimeter		Inches	
	Min.	Max.	Min.	Max.
A	19.81	20.32	0.780	0.800
B	20.80	21.46	0.819	0.845
C	15.75	16.26	0.620	0.640
∅D	3.15	3.65	0.124	0.144
E	4.32	5.49	0.170	0.216
F	5.40	6.30	0.213	0.248
G	1.65	2.18	0.065	0.086
H	3.80	4.50	0.150	0.177
J	1.00	1.40	0.039	0.055
K	10.80	11.10	0.425	0.437
L	4.70	5.30	0.185	0.209
M	0.40	0.80	0.016	0.031
N	1.50	2.49	0.059	0.098

G=Gate  
D=Drain  
S=Source



Symbol	Test Conditions	Maximum Ratings	Unit
$V_{DSS}$	$T_J=25^{\circ}C$ to $150^{\circ}C$	250	V
$V_{DGR}$	$T_J=25^{\circ}C$ to $150^{\circ}C$ ; $R_{GS}=1M\Omega$	250	V
$V_{GS}$	Continuous	$\pm 20$	V
$V_{GSM}$	Transient	$\pm 30$	V
$I_{D25}$	$T_C=25^{\circ}C$	50	A
$I_{DM}$	$T_C=25^{\circ}C$ ; pulse width limited by $T_{JM}$	150	A
$I_{AR}$	$T_C=25^{\circ}C$	50	A
$E_{AR}$	$T_C=25^{\circ}C$	15.8	mJ
$dv/dt$	$I_S \leq I_{DM}$ ; $di/dt \leq 100A/us$ ; $V_{DD} \leq V_{DSS}$ $T_J \leq 150^{\circ}C$ ; $R_G=2\Omega$	2.5	V/ns
$P_D$	$T_C=25^{\circ}C$	260	W
$T_J$		-55...+150	$^{\circ}C$
$T_{JM}$		150	
$T_{stg}$		-55...+150	
$T_L$	1.6mm(0.062 in.) from case for 10s	300	$^{\circ}C$
$M_d$	Mounting torque	1.13/10	Nm/lb.in.
Weight	typical	6	g

# SMOS50N25B2

## Power MOSFETs

(T<sub>J</sub>=25°C, unless otherwise specified)

Symbol	Test Conditions	Characteristic Values			Unit
		min.	typ.	max.	
V <sub>DSS</sub>	V <sub>GS</sub> =0V; I <sub>D</sub> =250μA	250			V
V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> ; I <sub>D</sub> =4mA	3.0	3.5	4.0	V
I <sub>GSS</sub>	V <sub>GS</sub> =±20V <sub>DC</sub> ; V <sub>DS</sub> =0			±10	μA
I <sub>DSS</sub>	V <sub>DS</sub> =0.8V <sub>DSS</sub> ; T <sub>J</sub> =25°C			100	μA
	V <sub>GS</sub> =0V; T <sub>J</sub> =125°C			1.0	mA

(T<sub>J</sub>=25°C, unless otherwise specified)

Symbol	Test Conditions	Characteristic Values			Unit
		min.	typ.	max.	
R <sub>DS(on)</sub>	V <sub>GS</sub> =10V; I <sub>D</sub> =0.5I <sub>D25</sub> Pulse test, t <sub>s</sub> ≤300μs, duty cycle≤2%		0.050	0.065	Ω
g <sub>ts</sub>	V <sub>DS</sub> =10V; I <sub>D</sub> =0.5I <sub>D25</sub> ; pulse test	8	35		S
C <sub>ies</sub>	V <sub>GS</sub> =0V; V <sub>DS</sub> =25V; f=1MHz		3000		pF
C <sub>oes</sub>			500		
C <sub>res</sub>			55		
Q <sub>g(on)</sub>	V <sub>GS</sub> =10V; V <sub>DS</sub> =0.5V <sub>DSS</sub> ; I <sub>D</sub> =0.5I <sub>D25</sub>		35	60	nC
Q <sub>gs</sub>			11	23	
Q <sub>gd</sub>			4	11	
t <sub>d(on)</sub>	V <sub>GS</sub> =10V; V <sub>DS</sub> =0.5V <sub>DSS</sub> ; I <sub>D</sub> =0.5I <sub>D25</sub> R <sub>G</sub> =2Ω (External)		10	15	ns
t <sub>r</sub>			18	25	ns
t <sub>d(off)</sub>			22	36	ns
t <sub>f</sub>			8	15	ns
R <sub>thJC</sub>				0.570	K/W
R <sub>thCK</sub>			0.211		K/W

### Source-Drain Diode

(T<sub>J</sub>=25°C, unless otherwise specified)

Symbol	Test Conditions	Characteristic Values			Unit	
		min.	typ.	max.		
I <sub>S</sub>	V <sub>GS</sub> =0V			50	A	
I <sub>SM</sub>	Repetitive; pulse width limited by T <sub>JM</sub>			150	A	
V <sub>SD</sub>	I <sub>F</sub> =I <sub>S</sub> ; V <sub>GS</sub> =0V; Pulse test, t <sub>s</sub> ≤300μs, duty cycle d≤2%			1.70	V	
t <sub>rr</sub>	I <sub>F</sub> =I <sub>S</sub> ; -di/dt=100A/μs; V <sub>R</sub> =100V;		140	200	ns	
		T <sub>J</sub> =25°C		280	400	ns
Q <sub>RM</sub>		T <sub>J</sub> =25°C		0.6		μC
		T <sub>J</sub> =125°C		1.2		μC
I <sub>RM</sub>			50		A	
		T <sub>J</sub> =125°C		50		A

**Sirectifier**<sup>®</sup>

# SMOS50N25B2

## Power MOSFETs

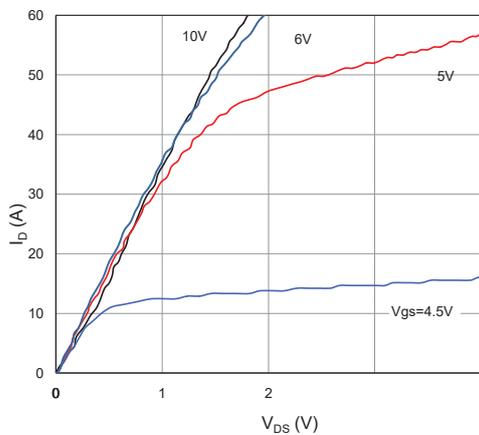


Fig 1. Typical Output Characteristics

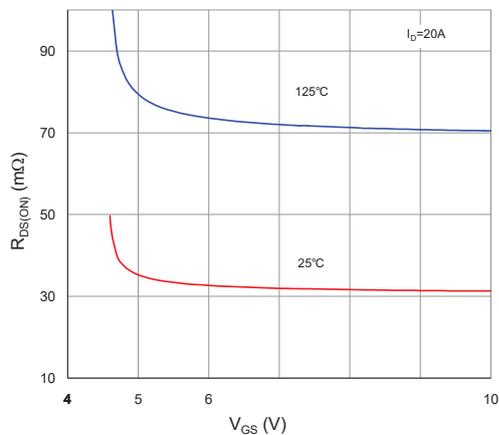


Figure 2. On-Resistance vs. Gate-Source Voltage

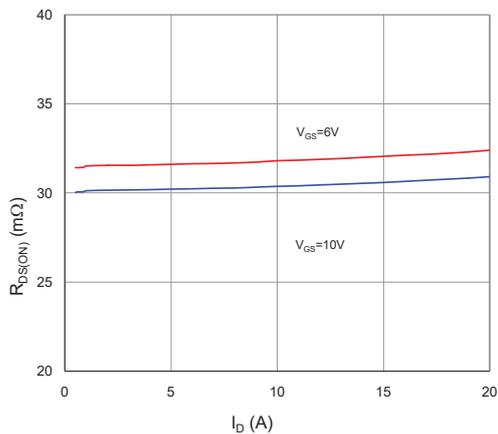


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

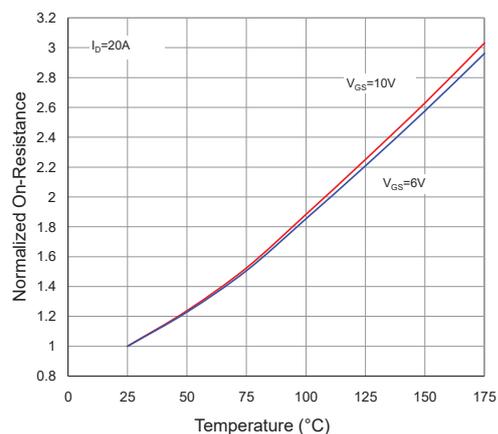


Figure 4. Normalized On-Resistance vs. Junction Temperature

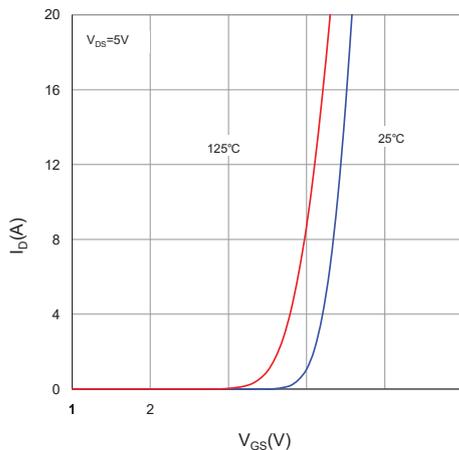


Figure 5. Typical Transfer Characteristics

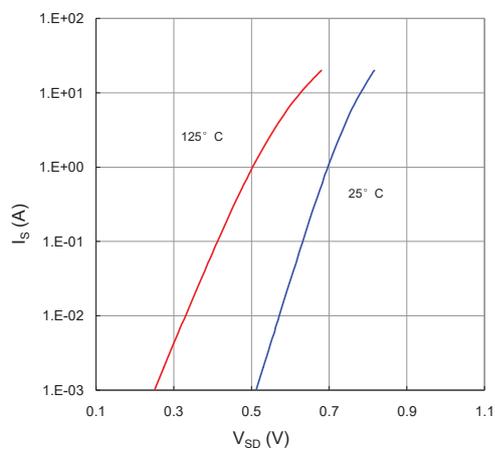


Figure 6. Typical Source-Drain Diode Forward Voltage

# SMOS50N25B2

## Power MOSFETs

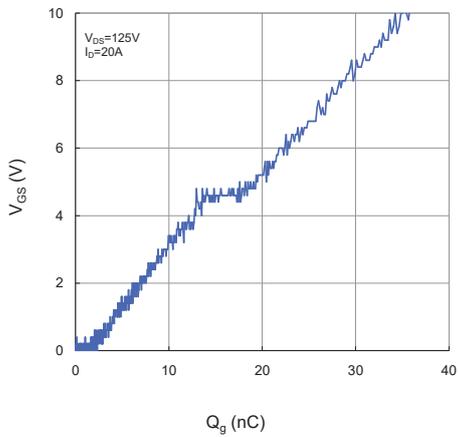


Figure 7. Typical Gate-Charge vs. Gate-to-Source Voltage

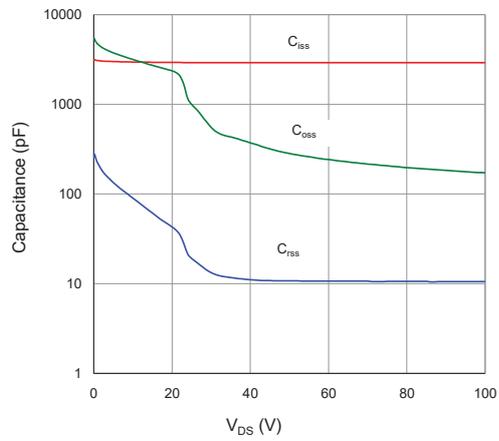


Figure 8. Typical Capacitance vs. Drain-to-Source Voltage

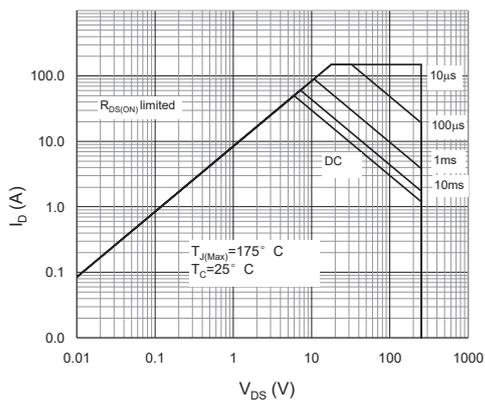


Figure 9. Maximum Safe Operating Area

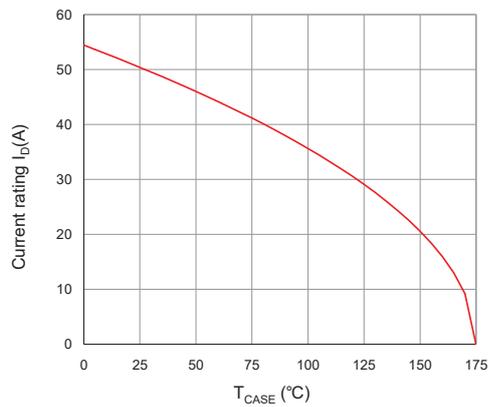


Figure 10. Maximum Drain Current vs. Case Temperature

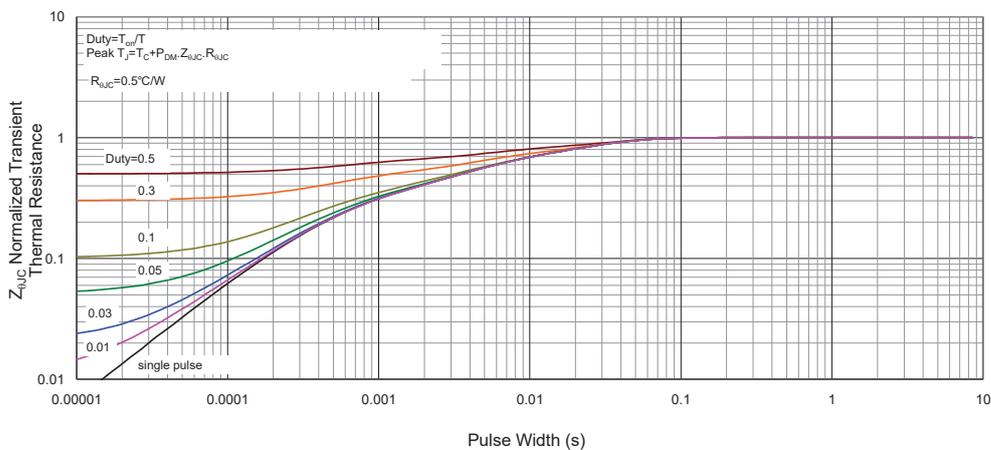


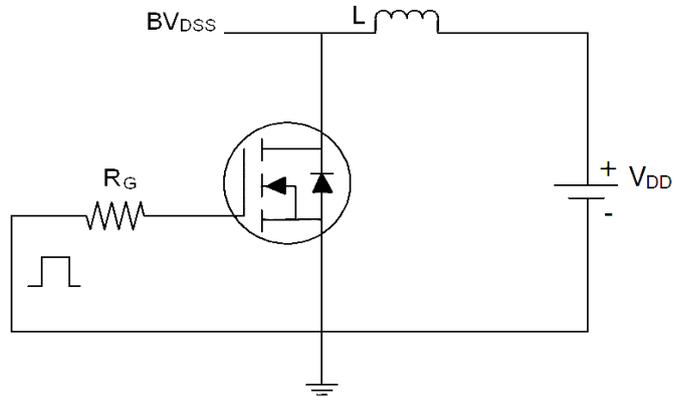
Figure 11. Normalized Maximum Transient Thermal Impedance, Junction-to-Case

# SMOS50N25B2

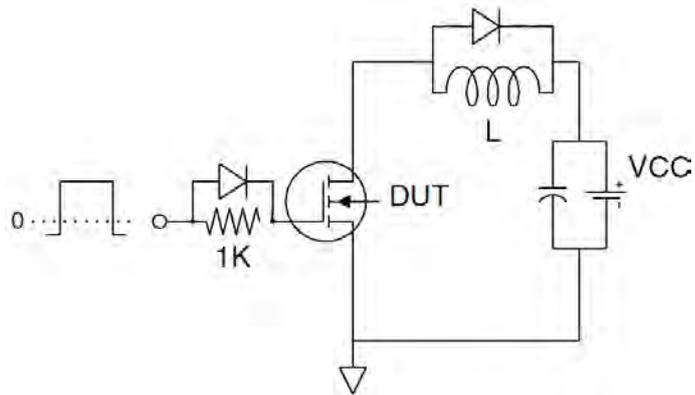
## Power MOSFETs

### Test Circuit

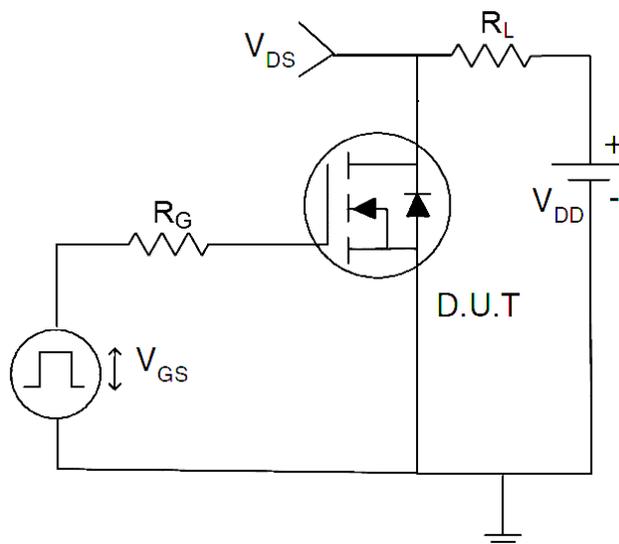
#### 1) $E_{AS}$ test Circuit



#### 2) Gate charge test Circuit



#### 3) Switch Time Test Circuit



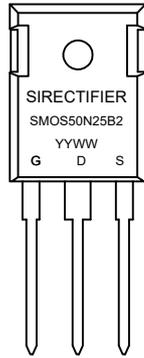
# SMOS50N25B2

## Power MOSFETs

### Marking

SMOS50N25B2

(TO-247AD)



Company Logo  
Part Number  
Lot No.



### Ordering Information

Part Number	Package	Shipping	Marking Code
SMOS50N25B2	TO-247AD	30pcs / Tube	SMOS50N25B2